

Advisory Committee

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Professor, Mangalore University, Mangaluru

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Professor, IIT, Hyderabad

Dr. N. Gopalakrishna Kini

Professor, MIT, Manipal

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Professor, Canara Engineering College, Mangaluru

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Professor, NMAMIT, Nitte

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Professor, NITK, Surathkal

Dr. Vasant. M. H.

Professor, NIT, Goa

Dr. Somashekar Bhat

Professor, MIT, Manipal

Dr. Ganesh Bhat

Principal, Canara Engineering College, Mangaluru

Dr. Ashok Rao

Former Head, Network Project, CEDT IISc, Bengaluru

Dr. Usha Desai

Professor, NMAMIT, Nitte

Dr. H. N. Suresh

Professor, BIT, Bengaluru

Objectives

National conference on “Recent Trends in Electrical Science & Information Technology” aims to provide an opportunity for exchange of ideas and dissemination of information and to provide a platform for gathering of scientists, leading engineers, industry experts, research scholars and advanced student learners.

Organizing Committee

Chief Patron

Mr. Yenepoya Abdulla Kunhi

Honourable Chancellor,
Yenepoya Deemed to be University

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Mr. Farhaad Yenepoya, Pro-Chancellor,

Yenepoya Deemed to be University

Mr. Yenepoya Abdulla Javeed

Director Operations, Yenepoya Group

Mr. Moideen Khurshid

Director Purchase, Yenepoya Group

Chief Advisor

Dr. R. G. D'Souza

Principal, Yenepoya Institute of Technology

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HOD, Department of Electronics and Communication Engineering

Prof. Prasanna Kumar

HOD, Department of Electrical and Electronics Engineering

Prof. Guruprasad G

HOD, Department of Computer Science and Engineering

Prof. Pandu Naik

HOD, Department of Information Science and Engineering

Organizing Chair

Dr. Jeevan Pinto, Associate Professor, CSE Dept.

Dr. Sanjeev Kulkarni, Associate Professor, ISE Dept.

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NATIONAL CONFERENCE ON RECENT TRENDS IN ELECTRICAL SCIENCE AND INFORMATION TECHNOLOGY

RTESIT- 19

29th & 30th April, 2019



Organized by

Department of
Electronics and Communication Engineering
Electrical and Electronics Engineering
Computer Science and Engineering
Information Science and Engineering

In Collaboration with
International Journal of Engineering Research &
Technology



YENEPOYA INSTITUTE OF TECHNOLOGY

[Affiliated to Visvesvaraya Technological University, Belagavi]

NH- 13, Thodar,

Moodabidri- 574225, Mangaluru, D.K.

About Yenepoya Institute of Technology

Yenepoya Institute of Technology was established in the year 2008 with the monumental objective of promoting academic excellence and competence in students, especially in the fast-growing global domain of Engineering Technology. The Institute made remarkable progress since its inception in every aspect and gained name among the institutes of technology in the state of Karnataka due to the quality of education and training provided by its dedicated faculty and the infrastructure available. This renowned Institute is now owned by the Islamic Academy of Education of Yenepoya Group of Institutions and Yenepoya Deemed to be University, an organization well known for Quality Education.

Yenepoya Institute of Technology is located in the picturesquely beautiful Moodabidri, Mangaluru, Karnataka. The college stands out for its magnificent buildings and ultra-modern infrastructure; a fascinating blend of architectural excellence and functional values. YIT is committed to the ideals which it preserves and believes in. Yenepoya Institute of Technology has been a forerunner and prime mover in imparting technical education in Mangaluru, India. Affiliated to Visvesvaraya Technological University, approved by AICTE India, New Delhi. The Engineering College has grown by leaps and bounds since its inception in 2008.

About the Department

CSE & ISE

The Department of CSE & ISE started with the objective of promoting specialized knowledge in the field of Information Technology. The Department has a comprehensive curriculum on topics related to all aspects of Computer Hardware and Software with an emphasis on practical learning. Faculty members and students are encouraged in all academic activities.

ECE & EEE

The Departments of ECE & EEE are the major departments involving number of laboratories with a diversified variety of equipment being the core branches and with laboratories focusing on fundamental aspects of ECE & EEE. In order to cope up with the requirement of industries, the departments also have laboratories with latest technologies. The departments have a team of highly motivated and dedicated

faculty members for the cause of academics and striving to do the best in the interest of the engineering discipline and its students.

Conference Theme & Areas

The basic objective of the conference is to illustrate the emerging trends in the field of Electrical Science and Information Technology. The conference aims to provide common platform to academicians, research scholars and students to exchange their expertise and knowledge.

Full length original research or review articles are invited in the below mentioned themes (but not limited to)

Electrical Sciences

- Speech/Signal/Image Processing
- VLSI Design, Embedded System Design
- Wireless Communication
- MEMS and Nanotechnology
- Power Electronics, Power Engineering
- Smart grids, Solar Technology
- Electrical Engineering and Applications
- Conventional and Renewable Energy Production

Information Technology

- Machine learning, Artificial Intelligence
- Mobile Computing, Pattern Recognition
- Cloud Computing, Big Data Analytics
- Internet of Things, Network Security
- Wireless Sensor Networks

Paper Submission Guidelines

All papers will be peer reviewed on the basis of originality, technical depth, content & relevance to conference theme. Abstract of the papers has to be sent to rtesit@vit.edu.in along with registration form. Abstract should not exceed 250 words.

After intimation of acceptance, the authors are required to submit the full length paper along with registration form and fee payment details to the conference email. Full paper must include text, figures, graph, tables, references etc. not exceeding 6 pages. The paper should be in MS-Word and strictly as per the template available on website.

Publication

The research articles accepted and presented by the authors during the conference will be published in **International Journal of Engineering Research & Technology**.

Registration Fee

- Industry: ₹ 2,000/-
- Delegates from academic institution: ₹ 1500/-
- Research scholars & Students: ₹ 750/-
- Conference Attendee/ Participant/ Co-Author: ₹ 500/-

Bank Account Details

The details for the transfer of registration fees are given below:

Demand Draft:

In favour of: The Principal, Yenepoya Institute of Technology
Payable at: Moodbidri

NEFT/ RTGS:

Account Name	Principal YIT
Account Number	520101015085631
Branch	Corporation Bank, Moodbidri
IFSC Code	CORP0000598

Important Dates

- Submission of abstract: 28th February 2019
- Submission of full length paper: 15th March 2019
- Acceptance of full length paper: 25th March 2019
- Final Paper (Camera Ready) Submission & Registration: 15th April 2019

Important Instruction

Maximum four authors are allowed per paper. Registration kit and certificate of presentation will be given to the registered authors who present their paper in the conference. On request **skype** presentation opportunity will be provided.

Accommodation

Accommodation will be arranged on prior request with nominal charges.

For more details visit

<http://yit.edu.in/rtesit19>