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Electrical and Electronics Engineering

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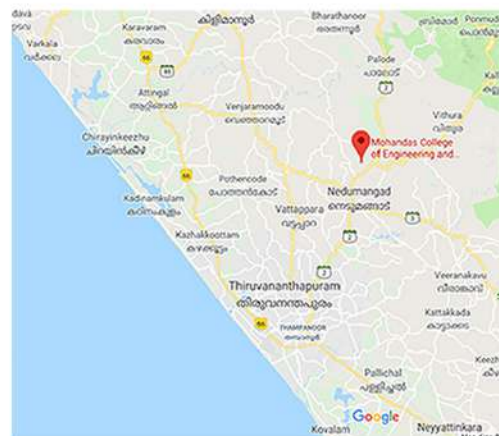
Mohandas College of Engineering and Technology
Anad, Nedumangad, Trivandrum.

REGISTRATION

The participants are requested to register their details at techsynod19@gmail.com. Accepted papers will be intimated by e-mail. They have to submit the camera ready paper along with the registration form and the registration fee as DD to: "The Principal, Mohandas College of Engineering and Technology", payable at Canara Bank, Anad, Trivandrum before 30/11/2019. Online transfer of registration fee may be by Net Transfer/RTGS to:

Account No. :2967201000037
IFSC code :CNRB0002967

ROUTE MAP



Trivandrum Airport
Domestic terminal - 27 km
Trivandrum Bus Station
Central Bus Station - 21 km

Railway Station
Trivandrum Central - 20 km
Kochu Veli - 2.5 km



MOHANDAS COLLEGE
OF ENGINEERING AND TECHNOLOGY

Anad, Nedumangad, Thiruvananthapuram-695544

An ISO 9001-2008 certified and NAAC accredited Institution



National Conference on Smart Grid Technologies, Applications & Signal Processing



Techsynod

Techsynod'19

(on 18th - 20th, December 2019)

Organised by

Departments of ECE & EEE



Tel: +91-472-2813039, 2802552, 2814578, 2814579

Email: mail@mcetonline.com URL: http://www.mcetonline.com

website: www.techsynod.com

Mission Committed to Excellence in Technology



ABOUT MCET

Mohandas College of Engineering and Technology (MCET) is a prestigious technical education project of the leading industrial group, "The Mohandas Group of Companies", Trivandrum. It is a NAAC Accredited and ISO 9001:2008 Certified Institution managed by V.N. Gangadhara Panicker Memorial Trust. The Institution is affiliated to APJ Abdul Kalam Technological University and the courses are approved by AICTE, New Delhi. The college offers B. Tech courses in Departments of Computer Science Engineering, Electronics and Communication Engineering, Electrical and Electronics Engineering, Mechanical Engineering, Civil Engineering and Biotechnology & Biochemical Engineering, M.Tech courses in Departments of Electronics & Communication Engineering, Electrical & Electronics Engineering and Computer Science Engineering and MCA course. MCET conducts International and National Conferences and seminars on a regular basis on topics of technical relevance. TECHSYNOD'19 is the 11th National conference and Workshop conducted by the college in this series.

CONFERENCE THEME

TECHSYNOD is a National level Conference held by MCET every year, jointly organized by two departments in the relevant area of interest. Department of Electrical & Electronics Engineering and Electronics & Communication Engineering in collaboration with IEEE PES Kerala Chapter, ANERT Kerala, EMC Kerala are organizing TECHSYNOD'19 the two day National Conference on Smart Grid Technologies, Applications and Signal Processing.

As the world is moving towards a dramatic change in the mode of Power Generation, Transmission and Distribution, Smart Grid is going to play an important role in mitigating the very ongoing concerns about climate change, rising demand for economic power and providing sustainable Electric Power for all. A Smart Grid facilitates drive towards generation from multiple and widely distributed renewable sources, like wind turbines, solar power systems, and plug-in hybrid electric vehicles. This will lead to decrease in adverse effects of pollution achieving conservation of energy and environment.

The smart grid implementation requires intelligent interaction between the power generating and consuming devices that can be achieved by installing devices capable of processing data and communicating it to various parts of the grid.

The efficiency of these devices is greatly dependent on the selection and implementation of the Advance Digital Signal Processing Techniques. In this regard, the challenges and limitations of the signal processing techniques need focussed research attention.

The conference will convene workshops, invited lectures and research paper presentations to provide an insight on challenges and latest trends in Smart Grid Technologies, Applications and Digital Signal Processing which will motivate greater interest and provide valuable information in these areas to researchers and practicing power engineers.

AREAS OF FOCUS

Integration of Distributed Resources
Smart Grid Architectures and Models
Electric Vehicles and Vehicle-to-Grid
Energy Efficient Technology
Photovoltaic Systems and Solar Energy Engineering
Renewable Energy Grid
Renewable Energy Utilization
Energy Storage and Distributed Energy Resources
High Voltage Systems and HVDC and FACTS Devices
Power Electronics and Drives.
Power Generation, Transmission and Distribution
Intelligent monitoring and outage management
Statistical signal processing
Concepts of signal processing using random signals, random vectors, random processes
Signal estimation and detection into software algorithms
Adaptive filters
Array signal processing
Optimal linear filter applications
Audio and Speech signal processing
Wavelet transform
Signal modelling and Statistical Decision Theory
Levinson recursion
Wiener filtering
Image and video Processing
Stochastic Control and Machine Learning

CALL FOR PAPERS

Authors are invited to submit original full research papers electronically in IEEE format in MS WORD 2007 and above to the following email id:

techsynod19@gmail.com

All the accepted papers will be published in International Journal of Engineering Research and Technology (IJERT) journal (ISSN : 2278 - 0181)

WHO CAN ATTEND AND BENEFIT?

The programme is expected to benefit Academicians, Researchers and practising Engineers in the area of Power Electronics Engineering, Applied Electronics, Electronics and Communication Engineering and Electrical engineering.

HOW TO REGISTER?

All authors and delegates are requested to register online at our website:

www.techsynod.com / www.mcetonline.com

A hard copy of the form may be downloaded and sent along with the registration fee as DD to

"The Principal, Mohandas College of Engineering and Technology", payable at Canara Bank, Anad, Trivandrum before 30/11/2019. Online transfer of registration fee may be by NEFT/RTGS to:

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IFSC Code :CNRB0002967

Fee details

Students/Research Scholars - Rs. 1500/-
Academicians - Rs. 1500/-
Technocrats from Industry - Rs. 2000/-

Important dates

	Due date	Extended Date
a. Submission of full paper	30/08/2019	08/11/2019
b. Acceptance notification	01/10/2019	22/11/2019
c. Camera ready paper	20/11/2019	30/11/2019

Accommodation

Accommodation can be provided on request in college hostel at normal charges.